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Amendments To the Claims:

Please amend the claims as shown. Applicants reserve the right to pursue any cancelled claims at a later date.

1.-11. (cancelled)

12. (new) A system for combining and representing signals of a hardware simulation device and elements of a listing of a software program, comprising:

a graphical display;

a software program;

a listing of the software program having listing elements; and

a hardware simulation device configured to:

simulate a behavior of a circuit having:

a processor;

a program memory including a program code of the software program; and

application-specific hardware components; and

create signals during a simulated execution of the program code included in the program memory, the program code corresponding to the listing elements, the signals representing a result of the hardware simulation, wherein the system is programmed and configured to:

interrelate the listing elements with the signals;

display the listing elements in a first partial area of the graphical display; and

display the signals in a second partial area of the graphical display.

13. (new) The system in accordance with Claim 12, further comprising a marking unit for marking at least one of the listing elements in the first partial area and for marking such signals interrelated with the at least one marked listing element in the second partial area.

14. (new) The system in accordance with Claim 12, wherein the graphical display comprises a third partial area for representing at least a part of the signals.

15. (new) The system in accordance with Claim 12, wherein the circuit is described in a hardware description language.

16. (new) The system in accordance with Claim 12, further comprising an adapting unit for adapting the system to different processor types.

17. (new) A method of combining and presenting signals of a hardware simulation device and elements of a listing of a software program, comprising:

> simulating a behavior of a circuit having:
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>> a processor;
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>> a program memory including a program code of the software program; and
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>> application-specific hardware components, by a hardware simulation device;
>
> creating signals during a simulated execution of the program code included in the program memory, the program code corresponding to the listing elements, the signals representing a result of the hardware simulation, by the hardware simulation device;
>
> interrelating the listing elements with the signals;
>
> displaying the listing elements in a first partial area of a graphical display; and
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> displaying the signals in a second partial area of the graphical display.

18. (new) The method in accordance with claim 17, further comprising:

> marking at least one of the listing elements in the first partial area; and
>
> marking such signals interrelated with the at least one marked listing element in the second partial area.

19. (new) The method in accordance with Claim 17, further comprising displaying at least a part of the signals in comprises a third partial area of the graphical display.

20. (new) The method in accordance with Claim 17, wherein the circuit is described in a hardware description language.

21. (new) The method in accordance with Claim 20, further comprising adapting such hardware description language corresponding to the processor to represent a different processor type.

22. (new)An error locating tool for combining and representing signals of a hardware simulation device and elements of a listing of a software program, the hardware simulation device configured to simulate a behavior of a circuit with a processor, with a program memory having a program code of the software program, and with application-specific hardware components, and the hardware simulation device further configured to create signals as a result of a simulation, the error locating tool comprising:

an analysis unit for interrelating the elements of the listing with the signals created during a simulated execution of the program code, the program code corresponding to the elements; and

a graphical display for displaying the elements in a first partial area of the graphical display and for displaying the signals interrelated with the elements in a second partial area of the graphical display.